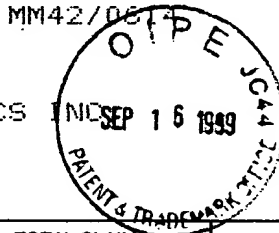


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Angie Rodriguez

(Signature)

Sept. 13, 1999

(Date)

APPLICATION NO.	FILING DATE	TOTAL CLAIMS	EXAMINER AND GROUP ART UNIT	DATE MAILED
09/007,668	01/15/98	022	MEIER, S	2822 06/14/99
First Named Applicant KALNITSKY, ALEX				

TITLE OF INVENTION: ENHANCED PLANARIZATION TECHNIQUE FOR AN INTEGRATED CIRCUIT

ATTY'S DOCKET NO.	CLASS-SUBCLASS	BATCH NO.	APPLN. TYPE	SMALL ENTITY	FEE DUE	DATE DUE
2 93-C-03203	257-644.000	U34	UTILITY	NO	\$1210.00	09/14/99

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1. Theodore E. Galanthay

2. Lisa K. Jorgenson

3. \_\_\_\_\_

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**PLEASE NOTE:** Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the PTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

STMicroelectronics, Inc.

(B) RESIDENCE: (CITY & STATE OR COUNTRY)

Carrollton, Texas

Please check the appropriate assignee category indicated below (will not be printed on the patent)

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Lisa K. Jorgenson

(Date)

8/30/99

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